

REMARKS

Claims 1-11 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections and objections are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to.

By separate letter, a proposed drawing correction has been submitted taking into consideration the helpful comments of the Examiner.

It is respectfully submitted that these drawing sheets are in full compliance with 37 CFR 1.121.

The title of the invention was objected to.

By the instant amendment, a new title is included.

It is respectfully submitted that the new title is clearly indicative of the invention in which the claims are directed.

Claims 1-8 were objected to because of informalities.

By the instant amendment, Claim 1 has been amended to take into consideration the helpful comments of the Examiner.

It is respectfully submitted that Claims 1-8 are free from informalities.

Turning now to the art rejection, Claims 1-11 were rejected under 35 U.S.C. § 102(b) as being anticipated by D'Angelo.

It is respectfully submitted that D'Angelo does not disclose or suggest the presently claimed invention including connecting the back gate of said PMOS-FET permanently to a voltage which is larger than the expression input pulse threshold voltage of the PMOS-FET minus diode voltage of a pn junction of the PMOS-FET.

D'Angelo discloses at column 7, lines 15 et seq. that the control unit 44 temporarily suspends the switch-mode operation of the voltage regulator 32 and consequently could not maintain voltage permanently.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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